AMENDMENTS TO THE CLAIMS

This listing of Claims will replace all prior versions, and listings, of Claims in the application.

1-43. (Cancelled)

44. (Previously Presented) A method of providing redundancy in a memory structure, comprising:

shifting out at least one first predecoder of a pluarality of first predecoders; and

shifting in a second predecoder.

45. (Previously Presented) The method of Claim 44, comprising shifting predecoded lines coupled to at least one of said first and second predecoders.

46. (Previously Presented) The method of Claim 44, comprising using shifting circuitry coupled to at least one of said first and second predecoders.

47. (Previously Presented) The method of Claim 44 comprising at least one higher address predecoded line coupled to at least said second predecoder.

48. (Previously Presented) The method of Claim 47 comprising at least one lower address predecoded line coupled to at least said active predecoder and paired with at least one higher address predecoded line.

49. (Previously Presented) A method of providing redundancy in a hierarchical memory structure, comprising:

identifying at least one first predecoder of a plurality of first predecoders;

shifting out said identified at least one first predecoder; and

shifting in at least one second predecoder.

50.   (Previously Presented) The method of Claim 49 comprising shifting predecoded lines coupled to at least one of said first and second predecoders.

51.   (Previously Presented) The method of Claim 49 comprising using shifting circuitry coupled to at least one of said first and second predecoders.

52.   (Previously Presented) The method of Claim 49 comprising at least one higher address predecoded line coupled to at least one of said first and second predecoders.

53.   (Previously Presented) The method of Claim 53 comprising at least one lower address predecoded line coupled to at least one of said first and second predecoders and paired with said at least one higher address predecoded line.

54.   (Previously Presented) The method of Claim 49 comprising at least one of said first predecoders adapted to fire for current address mapping.

55.   (Previously Presented) The method of Claim 49 comprising said at least one second predecoder adapted to fire for previous address mapping.

56.   (Previously Presented) The method of Claim 49 comprising shifting in said second predecoder using shift circuitry.

57.   (New) A method of providing redundancy in a hierarchical memory structure, comprising:

identifying at least one predecoder as a predecoder for shifting out; and

shifting out said identified at least one predecoder.

58.   (New) The method of Claim 57 comprising shifting in at least one other predecoder.

59.   (New) The method of Claim 58 comprising shifting predecoded lines coupled to at least one of said at least one predecoder and said at least one other predecoder.

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60.　(New) The method of Claim 58 comprising using shifting circuitry coupled to at least one of said at least one predecoder and said at least one other predecoder.

61.　(New) The method of Claim 58 comprising at least one higher address predecoded line coupled to at least one of said at least one predecoder and said at least one other predecoder.

62.　(New) The method of Claim 61 comprising at least one lower address predecoded line coupled to at least one of said at least one predecoder and said at least one other predecoder and paired with said at least one higher address predecoded line.

63.　(New) The method of Claim 57 comprising said at least one predecoder adapted to fire for current address mapping.

64.　(New) The method of Claim 57 comprising said at least one predecoder adapted to fire for previous address mapping.

65.　(New) The method of Claim 58 comprising shifting in said at least one other predecoder using shift circuitry.

66.　(New) A method of providing redundancy in a memory structure, comprising:

identifying at least one predecoder as a predecoder for shifting out; and

shifting in at least one other predecoder.

67.　(New) The method of Claim 66, comprising shifting predecoded lines coupled to at least one of said at least one predecoder and said at least one other predecoder.

68.　(New) The method of Claim 66, comprising using shift circuitry coupled to at least one of said at least one predecoder and said at least one other predecoder.

69.　(New) The method of Claim 66 comprising at least one higher address predecoded line coupled to at least one of said at least one predecoder and said at least one other predecoder.

70.   (New) The method of Claim 69 comprising at least one lower address predecoded line coupled to at least one of said at least one predecoder and said at least one other predecoder and paired with said at least one higher address predecoded line.

71.   (New) The method of Claim 66 comprising said at least one predecoder adapted to fire for current address mapping.

72.   (New) The method of Claim 66 comprising said at least one predecoder adapted to fire for previous address mapping.

73.   (New) The method of Claim 66 comprising shifting in said at least one other predecoder using shift circuitry.